

1. QC 2.0 Test Setup (HVDCP Simulator with E-load)

QC 2.0
Compliance
Charger

AC Line

D+	D-	Output	Switch Status
0.6 V	0.6 V	12 V	N1 = N2 = On, N3 = Off
3.3 V	0.6 V	9 V	N1 = On, N2 = N3 = Off
3.3 V	3.3 V	20 V	N1 = N2 = N3 = On
0.6 V	GND	5 V (default)	N1 = N2 = N3 = Off

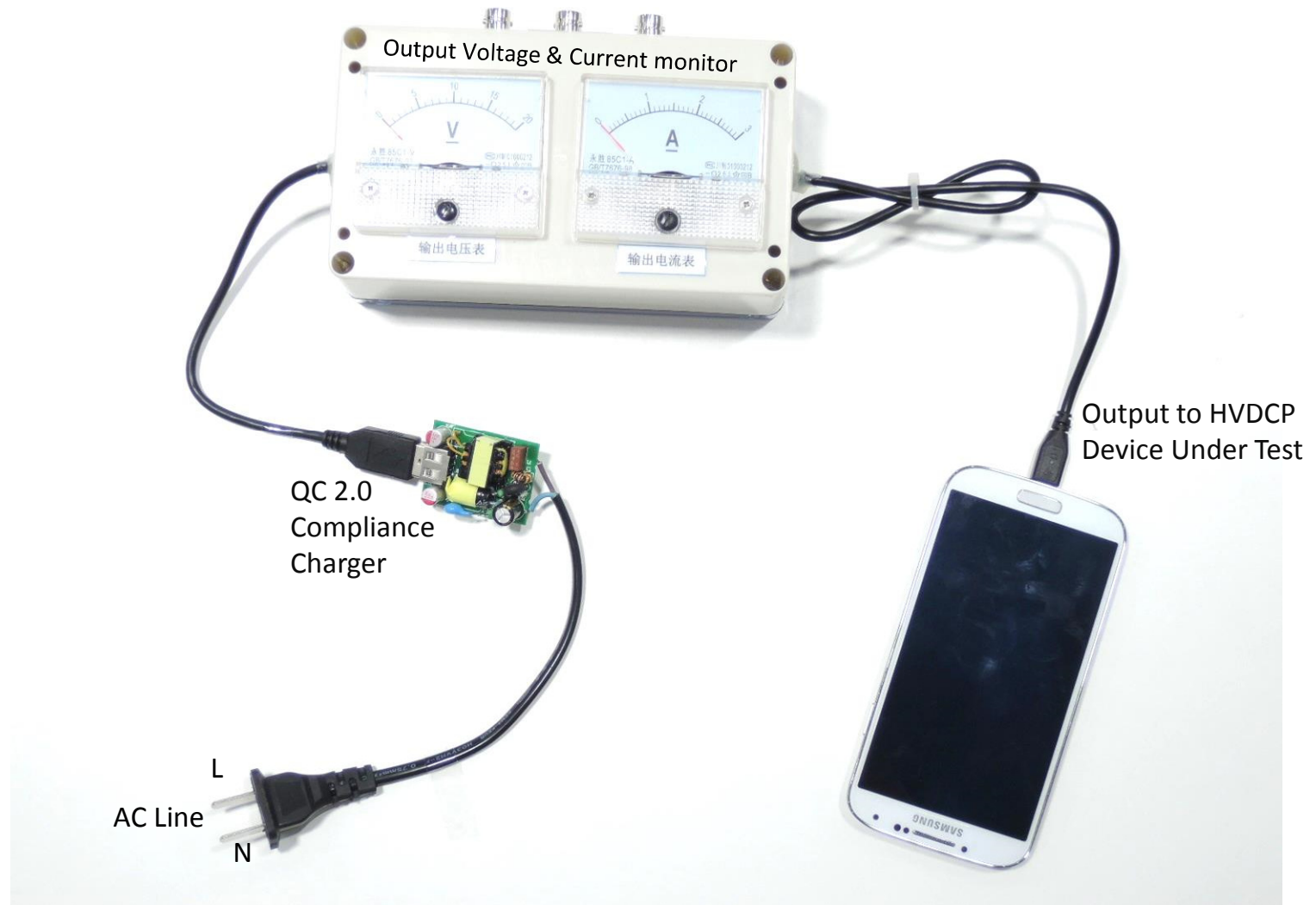
Table 1. Output Voltage Lookup Table.

HVDCP Simulator
(To select different
charging voltage)

Output to
Electronics
load

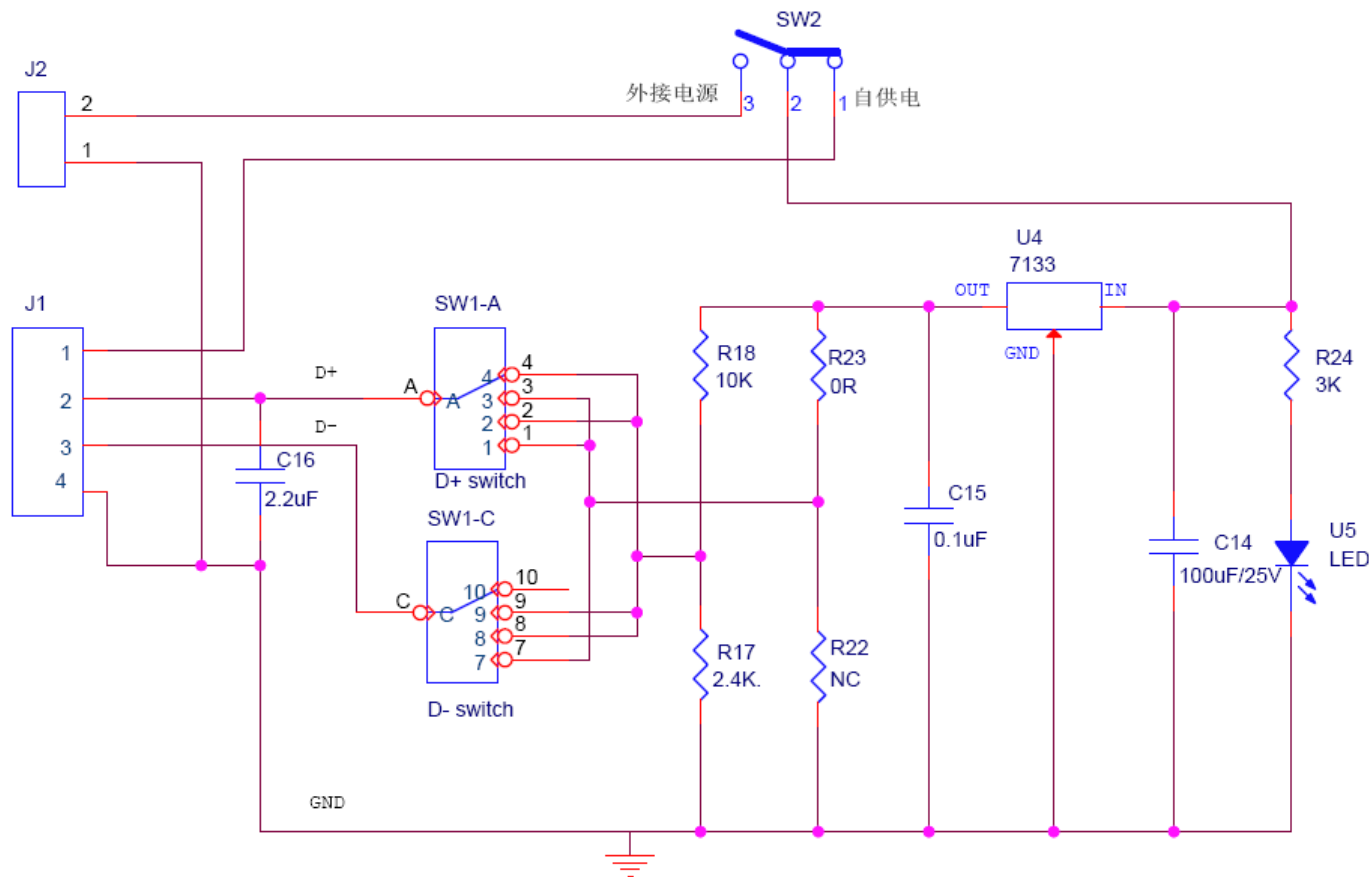
2. QC 2.0 Test Setup (HVDCP Device Under Test)

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3. Detail circuit of HVDCP Simulator

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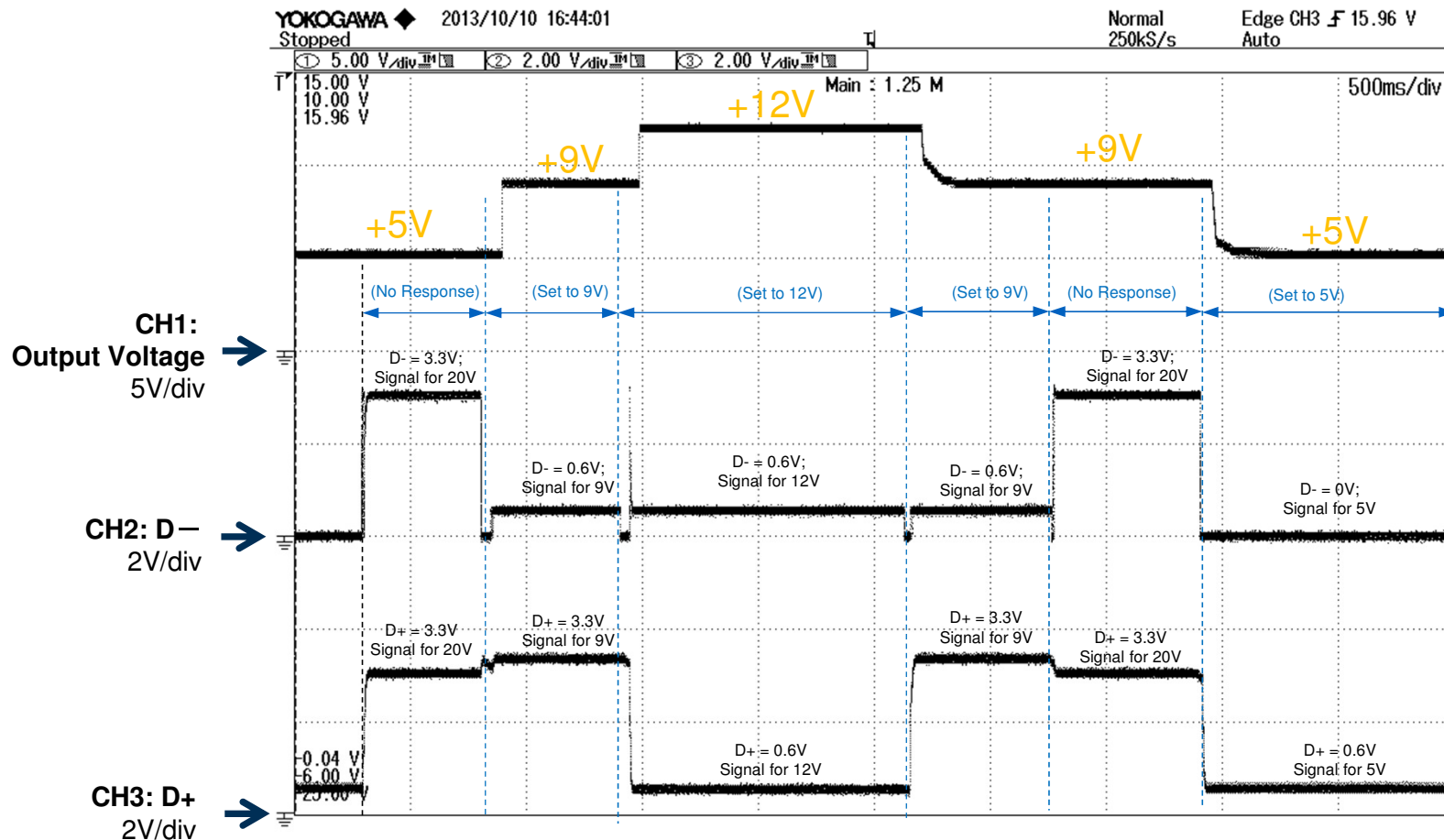
4. HVDCP Simulator Test and Test waveform

Test Signal for HVDCP

- Output at No-Load
- V1 & V2 pins connected with resistor on CHY100
- D+ / D- signal provided by HVDCP simulator.

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Table 1. Output Voltage Lookup Table.



High Level Quick Charger 2.0 Specification

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