LED Driver Datasheet

NU516

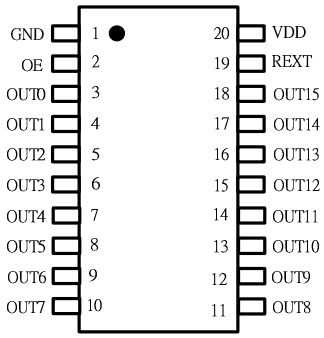
16 channel constant current LED driver

Features

- 16 constant current sink outputs
- 3 ~ 60mA channel sink current
- 3V to 5.5V supply voltage
- Excellent current sink uniformity
  channel to channel: ＜ ± 3%
  chip to chip: ＜ ± 3%
- OE pulse width: 120ns
- Schmitt trigger input
- 165℃ thermal shutdown protect
- 5ns output group delay for stagger output
- Maximum output voltage: 17V
- -40℃ ~ +85℃ operating temperature

Package Type

- TSSOP 173 mil 20 pin



Product Description

NU516 is a 16 channels constant current sink driver used for LED lighting. NU516 can sink 16 channels constant current simultaneously by the control of a single OE pin. The sink current of output channels can be set easily by an external resistor Rext. Each output channel can be connected with each other to gain higher current driving capability. With this parallel-able output capability, one NU516 can drive constant current from 3mA to 960mA being used to most types of LEDs.

Terminal Description

Pin name	Function
V_{DD}	5V/3.3V power supply
GND	Chip ground pin
R_{EXT}	Current setting resistor
OE	Output enable
OUT0 ~ OUT15	Constant current sink terminals

Applications

- General LED Lighting
- Decoration lighting for architecture
- LCD back lighting
- Street lamp

Protection Circuit

- 8KV output channel ESD protection

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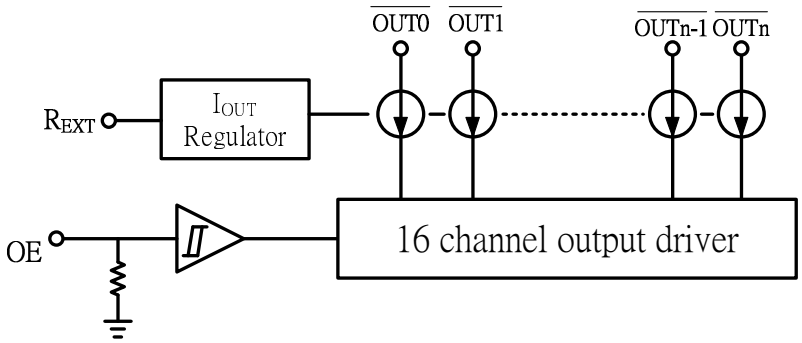
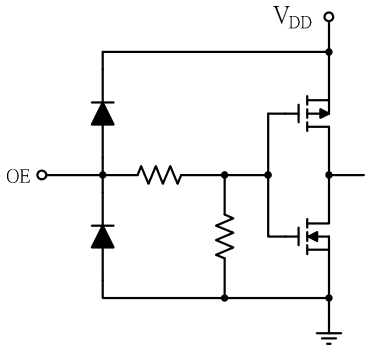
Block Diagram



Equivalent Circuits for OE Input



Maximum Ratings (T = 25˚C)

Characteristic	Symbol	Rating	Unit
Supply voltage	V_{DD}	0 ~ 6.0	V
Input pin voltage	V_{IN}	-0.2 ~ V_{DD}+0.2	V
Output current	I_{OUT}	80	mA/Channel
Output voltage	V_{OUT}	-0.2 ~ 24.0	V
Total GND terminals current	I_{GND}	1280	mA
Power Dissipation (On PCB)	PD	1	W
Thermal Resistance	$R_{TH(j-a)}$	33.3	˚C /W
Junction temperature	T_j	135	˚C
Operating temperature (Ambient)	T_{OPR}	-40~+85	˚C
Storage temperature	T_{STG}	-55~+150	˚C

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Electrical Characteristics and Recommended Operating Conditions

Characteristic		Symbol	Condition	Min.	Typ.	Max.	Unit
Supply voltage		V_{DD}	Room Temp.	3	5	5.5	V
Output port sustaining voltage		V_{OUT}	-	17	-	24	V
Output current		I_{OUT}	OUTn = 1V	3	-	60	mA
Output leakage		I_{LEAK}	V_O = 7V and channel off	-	-	0.5	uA
Channel current skew (Output)		dI_{OUT1}	I_{OUT} = 20mA, V_{OUT} = 1V	-	±1	±3	%
Center current skew (IC)		dI_{OUT2}	I_{OUT} = 20mA, V_{OUT} = 1V	-	-	±3	%
Line regulation		$\%/dV_{DD}$	R_{EXT} = 900Ω, V_{OUT} = 1V	-	±1	±2	%
Load regulation		$\%/dV_{OUT}$	R_{EXT} = 900Ω	-	±0.5	±1	%
Input voltage		V_{IH}		$0.7V_{DD}$	-	-	V
		V_{IL}		-	-	$0.3V_{DD}$	V
Thermal protect (Junction temperature)		T_{HalfP}	Half current output	-	135	-	˚C
		T_{Shut}	All output off	-	165	-	
Pull down resistor (OE)		R_{PU}		400	500	700	KΩ
Supply current	All output "Off"	$I_{DD1(off)}$	R_{EXT} = Open, all output off	-	1	-	mA
		$I_{DD2(off)}$	R_{EXT} = 900Ω, all output off	-	4	-	mA
		$I_{DD3(off)}$	R_{EXT} = 600Ω, all output off	-	5	-	mA
	All output "On"	$I_{DD1(on)}$	R_{EXT} = 900Ω, all output on	-	5	-	mA
		$I_{DD2(on)}$	R_{EXT} = 600Ω, all output on	-	6	-	mA

Switching Characteristics

Characteristic	Symbol	Condition	Min.	Typ.	Max.	Unit
Propagation Delay Time (OE from "L" to "H")	t_{pLH}	V_{DD}=4V, V_{OUT}=1V, I_{OUT}=120mA, OE= 0V → 4V	100	-	250	nS
Output current rising time (OE from "L" to "H")	t_{OERise}	V_{DD}=4V, V_{OUT}=1V, I_{OUT}=120mA, OE= 0V → 4V	100	-	250	nS
Propagation Delay Time (OE from "H" to "L")	t_{pHL}	V_{DD}=4V, V_{OUT}=1V, I_{OUT}=120mA, OE= 4V → 0V	100	-	500	nS
Output current falling time (OE from "H" to "L")	t_{OEFall}	V_{DD}=4V, V_{OUT}=1V, I_{OUT}=120mA, OE= 4V → 0V	100	-	250	nS

Test Circuit for Switching Characteristics

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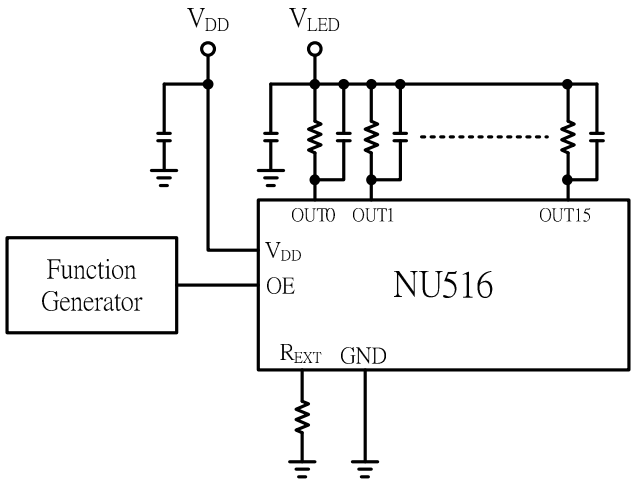
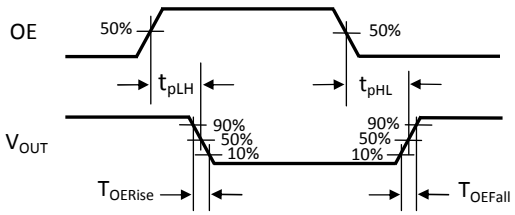
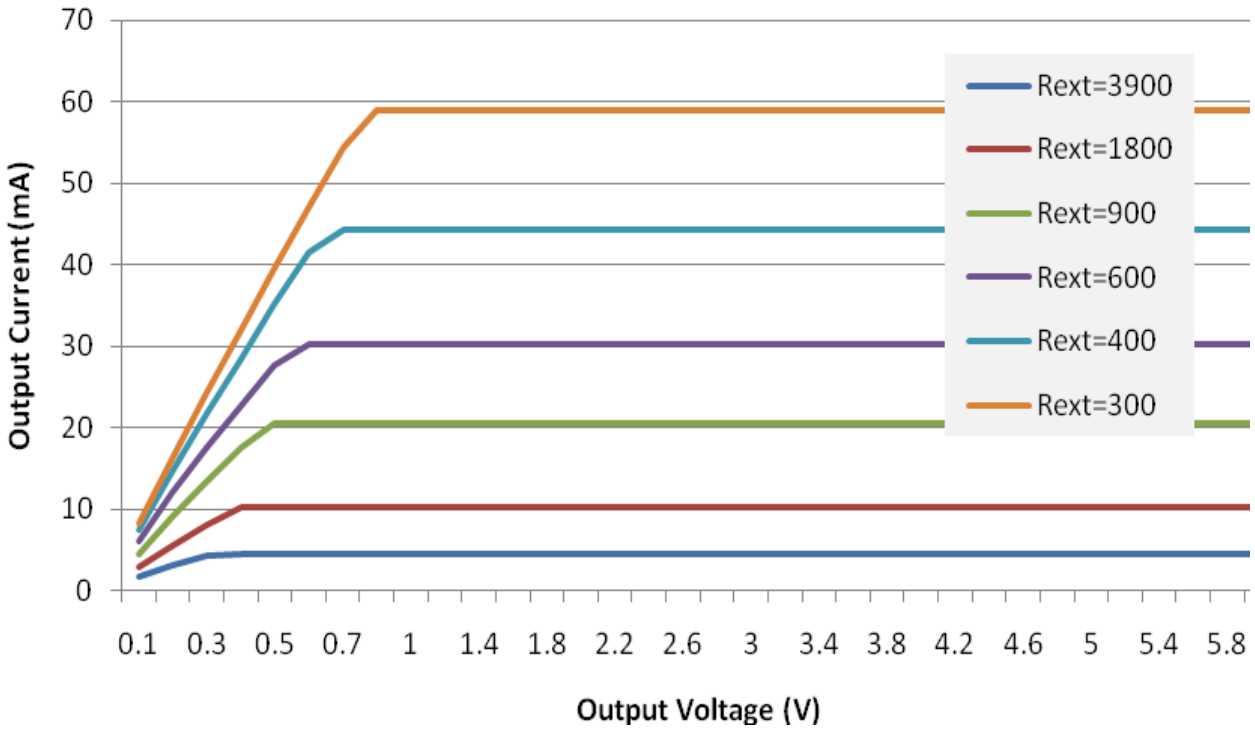


Timing Waveforms



I/V curve



Output Current Setting

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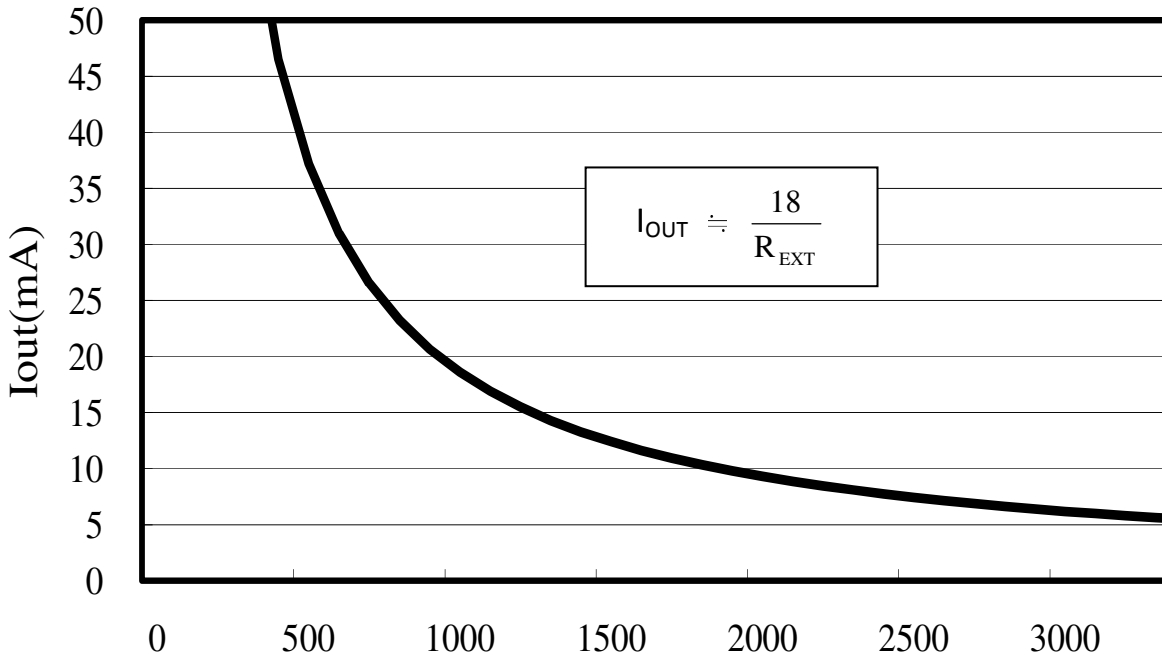
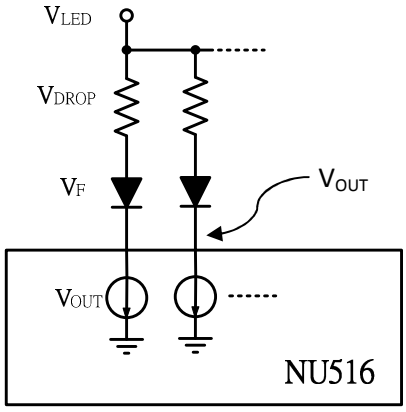
The output current of each channel of NU516 is set by an external resistor (R_{EXT}). The relationship between output current and external resistor is shown in the figure or calculated from the equation following.

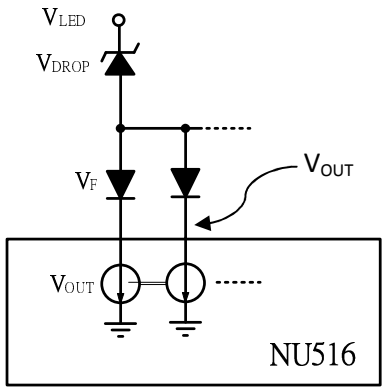


Application Notes

- In order to maximize the heat dissipation capability and keep the NU516 function normally, the thermal pad under TSSOP package should be soldered to the PCB and connect to the ground net of system. More the ground area, more the heat dissipation capability that NU516 relies on.
- The V_{OUT} should be as low as possible near the knee point of the output I/V curve to minimize the heat generation from NU516. An external resistors or zener diodes can be used to minimize V_{OUT} in the output current path. The suggestion V_{OUT} voltage is between 0.4v to 1v.

Ex: $V_{OUT} = V_{LED} - (V_{DROP} + V_F)$





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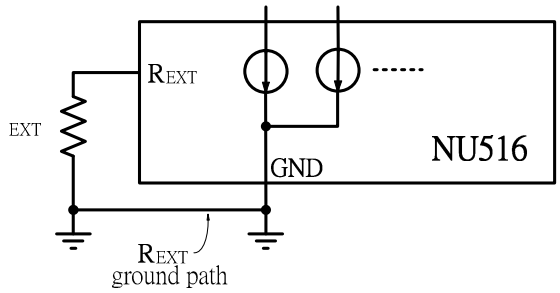
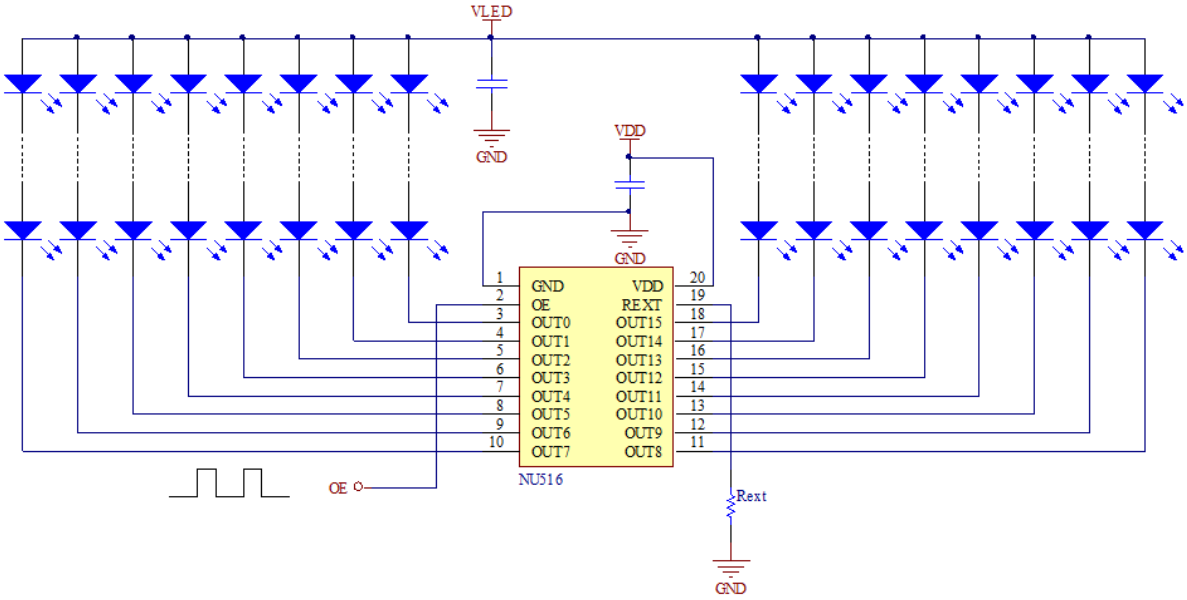
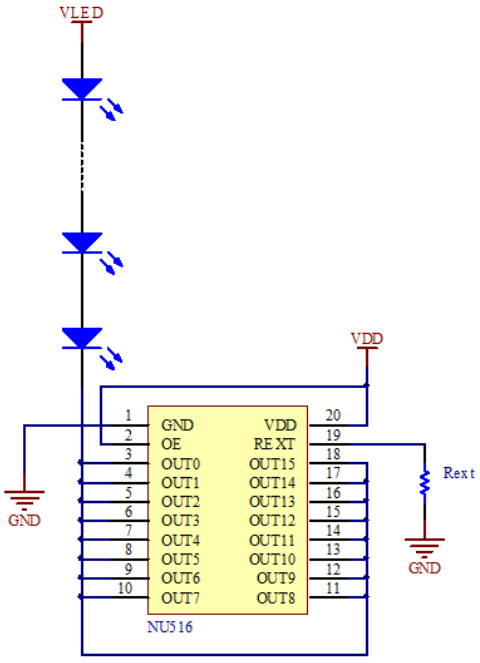
- The R_{EXT} ground path should be as short and wide as possible to minimize the chip current skew.



Typical Application Circuit





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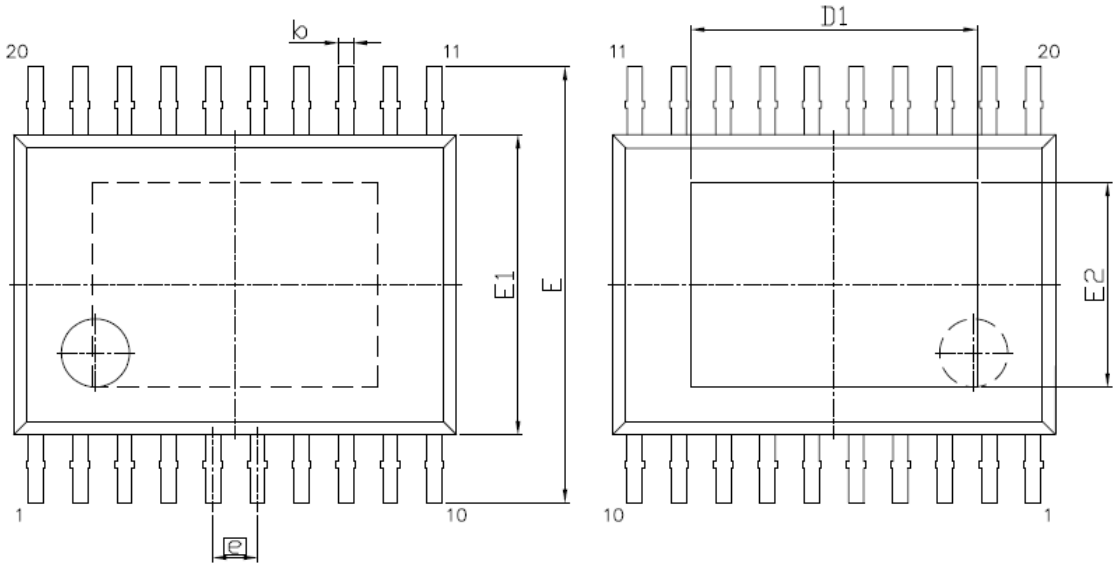
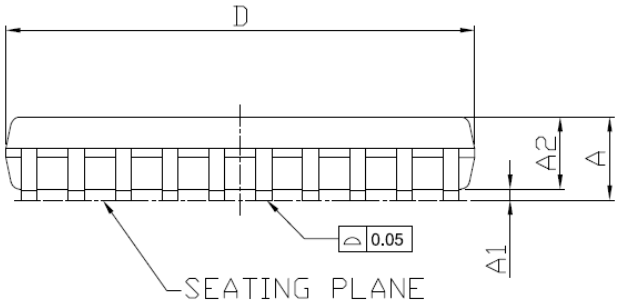
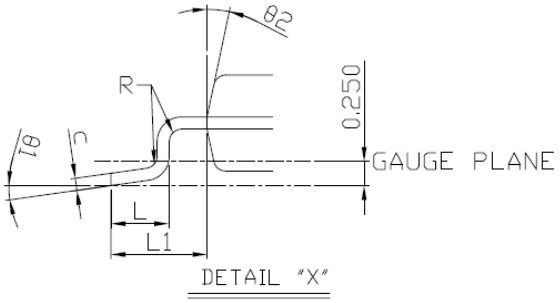
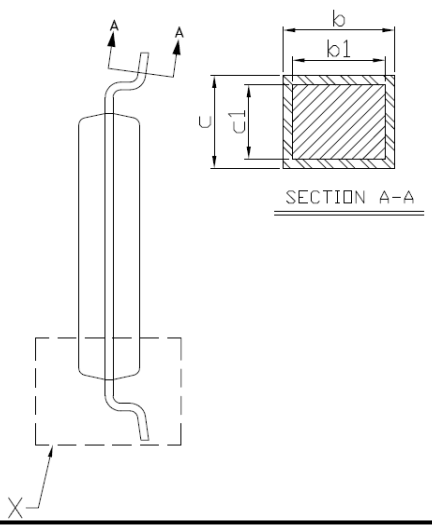
Package Dimensions





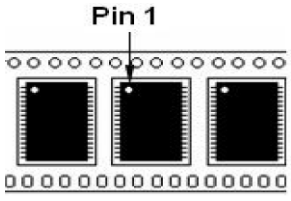


SYMBOL	DIMENSION (MM)			DIMENSION (MIL)		
	MIN.	NOM.	MAX.	MIN.	NOM.	MAX.
A	–	–	1.20	–	–	47.0
A1	0.00	–	0.15	0.0	–	5.9
A2	0.80	1.00	1.05	31.5	39.4	41.3
b	0.19	–	0.30	7.5	–	11.8
b1	0.19	0.22	0.25	7.5	8.7	9.8
c	0.09	–	0.20	3.5	–	7.9
c1	0.09	–	0.16	3.5	–	6.3
D	6.40	6.50	6.60	252.0	256.0	260.0
D1	3.80	4.00	4.20	149.6	157.0	165.0
E		6.40 BSC			252.0 BSC	
E1	4.30	4.40	4.50	169.3	173.2	177.2
E2	2.60	2.80	3.00	102.3	110.0	118.0
e		0.65 BSC			25.6 BSC	
L	0.45	0.60	0.75	17.7	23.6	29.5
L1		1.00 REF			39.4 REF	
R	0.127	–	–	5.0	–	–
θ1	0°	–	8°	0°	–	8°
θ2		12° REF			12° REF	



Taping Specification

PACKAGE	Q'TY/REEL
TSSOP	2,500 ea



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